FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

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ATTY. DOCKET NO. 1875.2800001	APPLICATION NO. 10 720/44
APPLICANT Mulder et al. , /	
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ru	AB1		554,943	09/1996	Moreland	327	65	
me	AC1		118,971	06/1992	Schenck	326	32	
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INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.

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OTHER (Including Author, Title, Date, Pertinent Pages, etc.) Flynn, M. and Sheahan, B., "A 400-Msample/s, 6-b CMOS Folding and Interpolating ADC," IEEE Journal of Solid-State Circuits, IEEE, Vol. 33, No. 12, December 1998, HU AR 3 pp. 1932-1938. Geelen, G., "A 6b 1.1GSample/s CMOS A/D Converter," IEEE International Solid-M AS 3 State Circuits Conference, IEEE, 2001, pp. 128-129 and 438. Hoogzaad, G. and Roovers, R., "A 65-mW, 10-bit, 40-Msample/s BiCMOS Nyquist ADC in 0.8 mm³," IEEE Journal of Solid-State Circuits, IEEE, Vol. 34, No. 12, AT 3 December 1999, pp. 1796-1802.

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M	Kusumoto, K. et al., "A 10-b 20-MHz 30-mW Pipelined Interpolating CMOS ADC," IEEE Journal of Solid-State Circuits, IEEE, Vol. 28, No. 12, December 1993, pp. 1200-1206.							OS ADC," <i>IEEE</i> 1, pp. 1200-	
KU	Lewis, S. et al., "A 10-b 20-Msample/s Analog-to-Digital Converter," IEEE Journal of Solid-State Circuits, IEEE, Vol. 27, No. 3, March 1992, pp. 351-358.							IEEE Journal 8.	
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All	AS	<u>6</u>	Nagaraj, K End," <i>IEEE</i> pp. 1056-1	E Journal of S	fficient 6-Bit A/D Converter Solid-State Circuits, IEEE,	Using a 1 Vol. 34, N	I-Bit Fold To. 8, Aug	ing Front ust 1999,
ja	AT .	<u>6</u>	Converter	in a $0.25-\mu m$ I	Dual-Mode 700-Msamples/s 6- Digital CMOS," <i>IEEE Journal</i> Der 2000, pp. 1760-1768.	bit 200-Ms of Solid-	amples/s State Cir	7-bit A/D cuits, IEEE,
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FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT ATTY. DOCKET NO. 1875.2800001

APPLICATION NO. To Be Assigned

APPLICANT Mulder et al

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INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO. 1875.2800001	APPLICATION NO. To Be Assigned 10 /28/44
APPLICANT Mulder et al. /	
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pa	AS	<u>8</u>			, M.R., "A 100-MS/s 8- ce from 3.8 V Down to 36, No. 3, March 2001		ADC with	Sustained id-State
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APPLICANT Mulder et al.	

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INFORMATION DISCLOSURE STATEMENT

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N	AS	<u>10</u>	Yu, P.C. and Lee, H-S., "A 2.5-V, 12-b, 5-Msample/s Pipelined CMOS ADC," IEEE Journal of Solid-State Circuits, IEEE, Vol. 31, No. 12, December 1996, pp. 1854- 1861.							
ral	АТ	10	Miyazaki et al., "A 16mW 30MSample/s 10b Pipelined A/D Converter using a Pseudo- Differential Architecture", ISSCC 2002/Session 10/High-Speed ADCs/10.5, February 5, 2002, 3 pgs.							
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